

Figure 1

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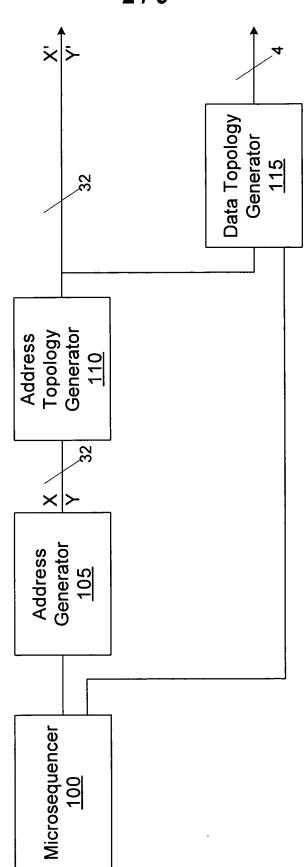
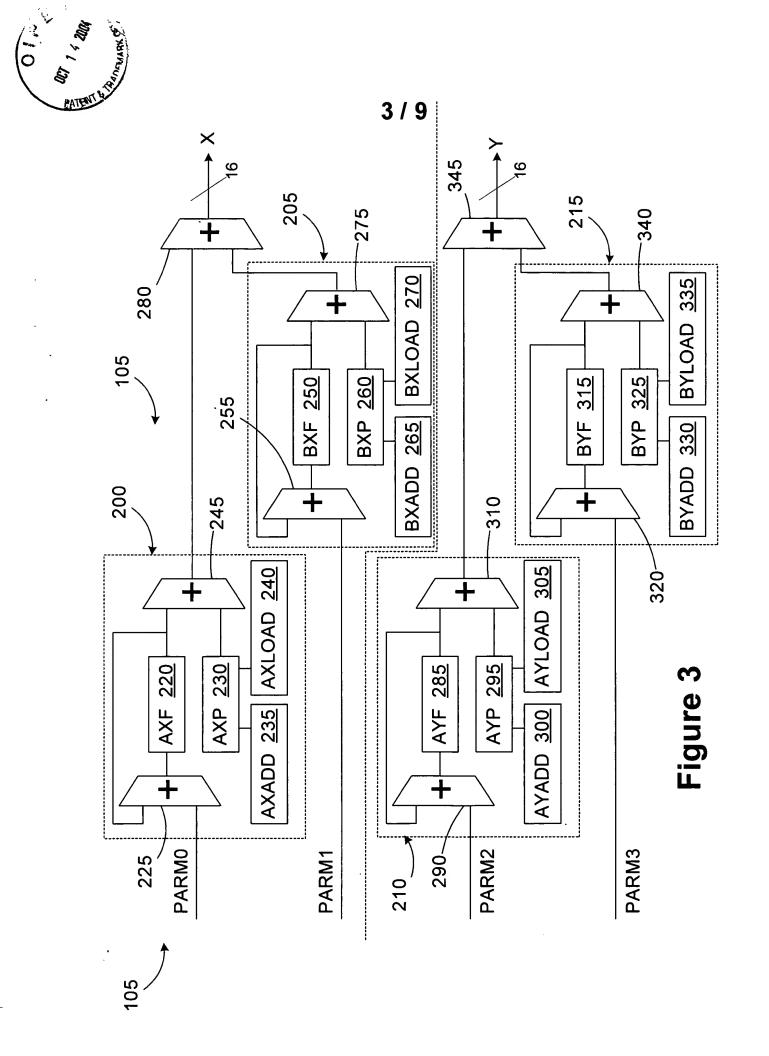
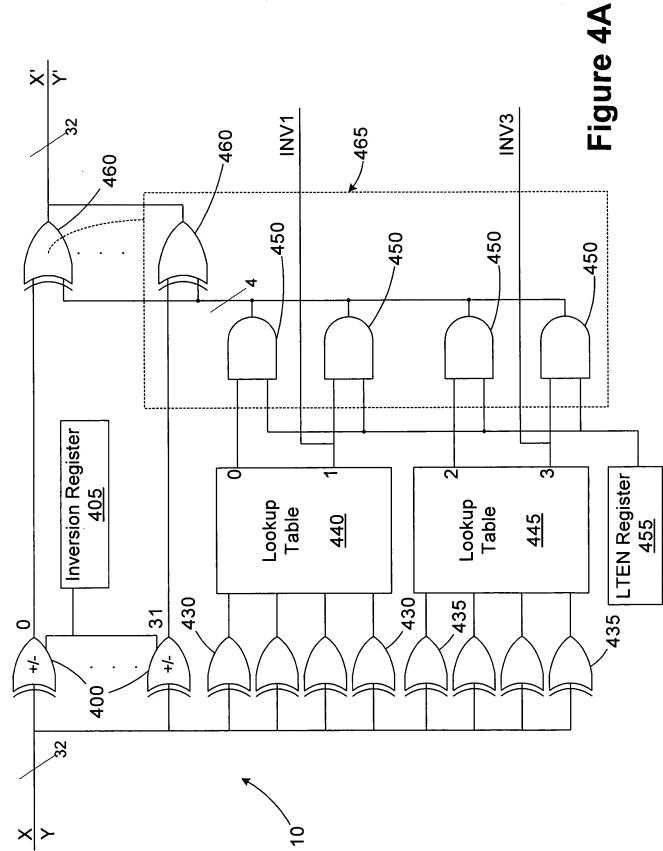


Figure 2







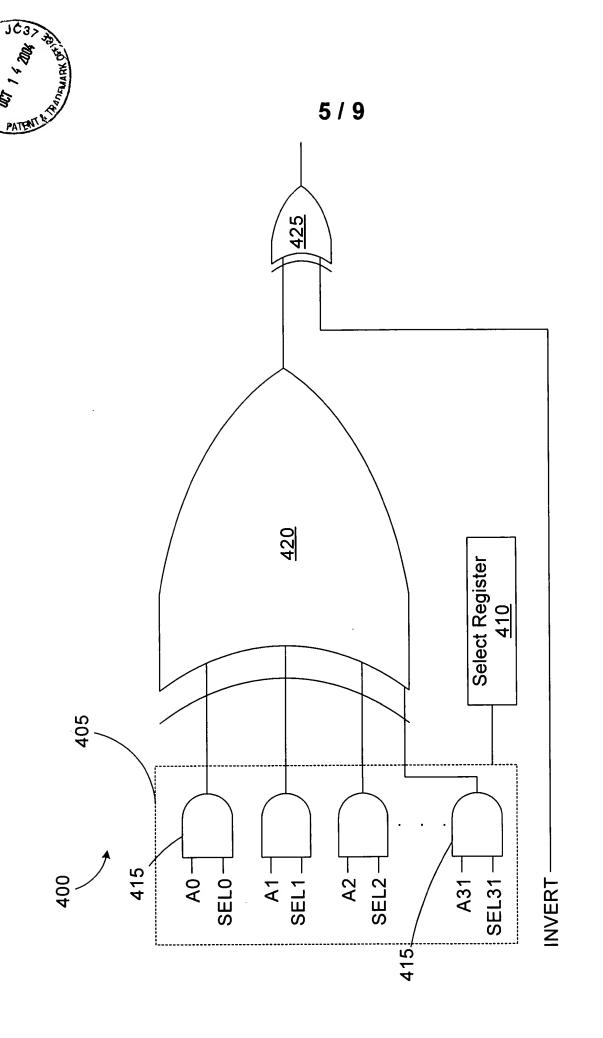
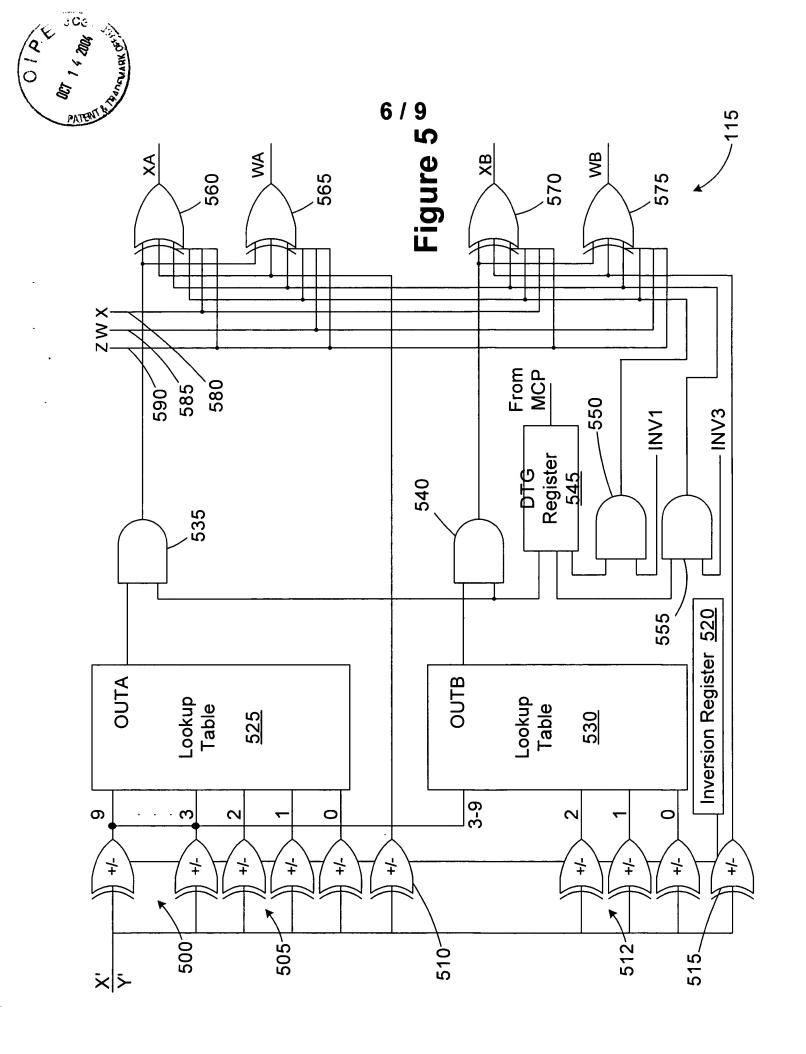
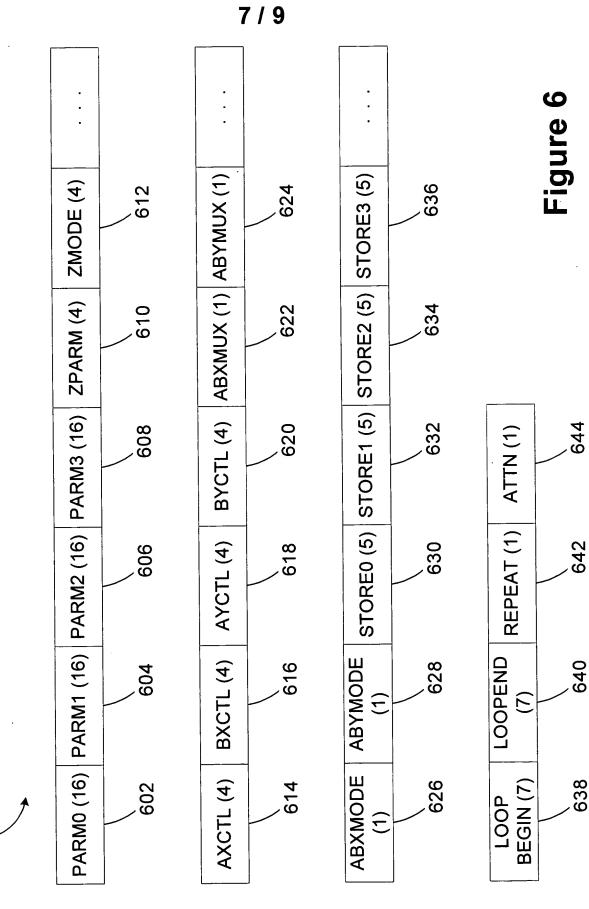


Figure 4B







Loop IP (2) 752

Loop IP (3) 753

Loop IP (0)
750
Loop IP (1)
751



Loop Init 730

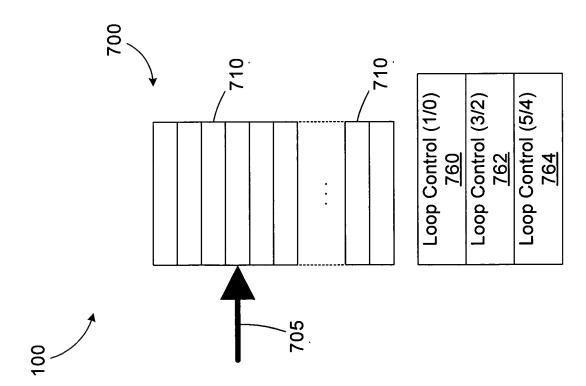
Repeat Count	Repeat Counter
715	717
Loop Count (0)	Loop Counter (0)
<u>720</u>	740
Loop Count (1)	Loop Counter (1)
721	741
Loop Count (2)	Loop Counter (2)
722	742
Loop Count (3)	Loop Counter (3)
723	743
Loop Count (4)	Loop Counter (4)
724	744
Loop Count (5)	Loop Counter (5)
725	745
Loop Count (6)	Loop Counter (6)
726	746
071	7

Loop IP (4) 754

Loop IP (5) 755

Loop IP (6) 756

Figure 7



	9 / 9																OT 1 4 200										
ВХСТ	616	4	4	4	9																		_	T to	5]	
AXCTL	614	2	4	4	4		STORE0	630	1F	1F	11	80	ATTN	ATTN	644	0	0	0	0		, marking start		962	f iterations for 1	er (740-746)		
ZMODE	612	0	0	0	0		ABYMODE	628	0	0	0	0		REPEAT	642	0	0	0	1		1st instruction.			ining number o	store remaining number of iterations for 1st loop in a loop counter (740-746)		
ZPARM	610	0	0	0	0		ABXMODE	626	0	0	0	0		LOOP END	640	00	00	00	03	8	Identify		794	store rema	i dool	Figure 7A	
PARM3	608	0000	0000	0000	0000		ABYMUX	624	0	0	0	0		LOOP BEGIN	638	01	02	00	00	Figure 8			792	equent	ounter	Figu	
PARM2	909	0000	01FF	3444	1000		ABXMUX	622	_	0	0	0		STORE3	<u>636</u>	1F	- 1F	1F	1F		700) indexed by r (705)	798	05) with a subse	remaining for 1st loop is zero (e.g., loop counter [740-746] equals zero)			
PARM1	604	0000	0000	0000	0001		BYCTL	<u>620</u>	4	4	4	4		STORE2	634	1F	1F	1F	18			Store intructions in queue (700) indexed by instruction pointer (705)	7	Load instruction pointer (705) with a subsequent loop instruction pointer value if number of iterations	or 1st loop is zero (e.g., 1740-746] equals zero)		
PARMO	<u>602</u>	07FF	0000	0003	FFFF		AYCTL	618	4	2	9	4		STORE1	632	4	1F	1F	1F			Store intruc		Load instru-	remaining f		
	200	802	,	803~	804 T))				L			L						·J	I							